Nordcad Seminars

Stockholm - Copenhagen

Nordcad

> ⓘ **Registration is free, but binding**
> If you fail to attend the seminar without notifying Nordcad in advance, a no-show fee of 800 DKK will be charged.

What's going to happen?

On October 8th and 9th, Nordcad invites you to two free one-day seminars in Stockholm and Copenhagen. The events showcase the latest innovations in Allegro X and OrCAD X, Release 25.1.

This is the agenda overview*. The agenda will be the same in both Stockholm and Copenhagen.

* Keep in mind that the agenda may be subject to changes.

Joint agenda (from 08:30 to 11:00)

From 08.30 to 11.00, we will have a joint agenda. After that, you're able to go out on tracks, of which you can pick and choose as you please.

Time:	Description:
08:30	Warm welcome and light breakfast
09:00	General introduction
09:10	Cadence pitch (Vision, Flow)
	Presented by Mark Hepburn
10:00	Break

| 10:15 | Bringing Electronic design to the next level with Cadence AI and Allegro X AI.

Presented by Mark Hepburn |
| 11:00 | Break / End of joint agenda |

Agenda for tracks #1 and #2 (from 11:15 to 16:00)

Time:	Track #1	Track #2
11:15	Unlock the full power of OrCAD X Layout with its modern, easy-to-use features — including revolutionary built-in analysis and wizard-driven constraint management.

Presented by Morten Rovsing, AE at Nordcad, and Ole Ejlersen, CTO at Nordcad | Schematic is no longer just schematic design — it's an integrated solution with analysis and true shift-left technology (incl. ease of use, team design, audit, PFM, version-on-save, variants, part management, etc.).

Presented by Aditya Chandra, Product Engineering Architect at Cadence |
| 12:00 | Lunch break | |
| 13:00 | Get all the benefits of designing with OrCAD:

- Frontend design with part management.
- Direct access to the cloud (libraries with models, sharing, etc.)

Presented by Nitin Thapliyal, Principal Product Engineer at Cadence | Next-level design with Allegro technology (integrated system capture, design, and analysis). IDA, stress, MTBF, AWR, Thermal, Integrated PCB Design, etc.

Presented by Aditya Chandra, Product Engineering Architect at Cadence |

13:45	Break	
14:00	Learn how to evaluate and compare simulation results across different manufacturers easily. Discover new model types that enable deeper analysis than ever before, and improve schematic documentation by aligning all settings, texts, and more. *Presented by Nitin Thapliyal, Principal Product Engineer at Cadence*	Embedded cockpit with schematic, PCB Design, reliability analysis, MTBF, SI/PI, and thermal analysis *Presented by TBA*
14:45	Break	
15:15	Speed up and automate collaboration with review and markup on schematic and PCB designs across teams and locations. **More TBA**. *Presented by Nitin Thapliyal, Principal Product Engineer at Cadence*	Latest and greatest of Allegro X and OrCAD X *Presented by TBA*
16:00	End of the day	

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