Nordcad

AWR Seminar 2025, Thursday, March 27th

Here is the full agenda for our upcoming one-day AWR seminar in Stockholm. The topics and included presentations are below.

All presentations will be in English.

08:30 A warm welcome and breakfast.

09.30

Planar Antennas in AWR – Axiem Simulations with Method of Moments.

Learn how to design and optimize planar antennas, such as patches, using **AWR's Planar 3D Method** of Moments solvers.

- Parameterize and optimize designs effortlessly.
- View 3D radiation patterns directly in the 3D layout.
- Explore planar arrays and see real-world examples for each design.

10.15 15-minute break.

10.30

3D Antennas in AWR

Discover the design of 3D antennas like waveguide horns and dielectric antennas using **AWR's 3D FEM solvers**.

- Easily parameterize and optimize designs.
- View 3D radiation patterns in the 3D layout.
- Address unwanted "antennas" like PCB connector transitions or vias without back drills. Real-world examples will be demonstrated.

11.15 15-minute break.

11.30 **Phased Array Antenna Synthesis and Simulation**

Discover phased array antenna design with Cadence AWR tools:

- Use **VSS Phased Array Antenna Synthesis** to create multi-patch arrays.
- Export to **Microwave Office** to add phase control circuitry and beam steering in real-time simulation.
- Demonstrate PCB import for integrated design. Examples will be shown and discussed in detail.

12.30 **Lunch break.**

13.30 **Optimizing PCB Antennas for Embedded Systems**

Learn to design and optimize a Bluetooth PCB antenna for Industrial IoT:

- Focus on size reduction and removing connectors while maintaining performance.
- Use **AWR and Allegro tools** for a "right-first-time" inverted F planar antenna design.

14.30 **15-minute break.**

14.45 **Advanced Antenna Design with Circuit/EM Co-Simulation**

Discover complete antenna design workflows:

- Use **phase array generator** for design, **VSS** for link budget analysis, and **Microwave Office** for active circuit integration.
- Explore how control electronics and PCB layout affect beam performance.
- Detailed project explanations and results are included.

16.00 **End of the conference and networking.**